

Attorney Docket No. 0756-2095

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Group Art Unit: 2814
Shunpei YAMAZAKI)	Examiner: N. Ngo
Serial No. 09/499,619)	<u>CERTIFICATE OF MAILING</u>
Filed: February 7, 2000)	I hereby certify that this correspondence is
For: ELECTRO-OPTICAL DEVICE AND)	being deposited with the United States Postal
METHOD FOR MANUFACTURING)	Service with sufficient postage as First Class
THE SAME)	Mail in an envelope addressed to:
)	Commissioner for Patents, P.O. Box 1450,
)	Alexandria, VA 22313-1450, on October 13,
)	2004. <u>Adeline M. Stamps</u>

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

In accordance with 37 C.F.R. § 1.97(e), it is certified that either (1) each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement, or (2) no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign patent application and no item of information contained in the information disclosure statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this information disclosure statement.


JP 58-004180 was cited by the Japanese Patent Office as it teaches a mobility of a polysilicon TFT is about 100 cm/V-sec. This reference teaches an active matrix substrate, which includes a matrix of data lines and gate lines. Peripheral driver circuits

for driving these data lines and gate lines, respectively, are formed on a peripheral portion of a substrate. A portion or all of the transistors which constitute the peripheral driving circuits has a higher mobility than the matrix circuit. The reference numeral 1 depicts a glass substrate, 2 is an active matrix circuit, 3 is a peripheral driving circuit, 4 is a polysilicon film, 5 is a CVD SiO₂ film, 6 is a polysilicon film, 7 is a CVD SiO₂ film, and 8 is an electrode.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280. A duplicate copy of this sheet is attached.

Respectfully submitted,



Eric J. Robinson
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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known	
		Application Number	09/499,619
		Filing Date	February 7, 2000
		First Named Inventor	Shunpei YAMAZAKI
		Group Art Unit	2814
		Examiner Name	N. Ngo
Sheet	1	of	1
		Attorney Docket Number	0756-2095

U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
		JP	58-004180			01/11/1983		Con. Stmt.

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Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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